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|-----------------------------------|---------------------------------------|---|-------------|
| <b>Notice of References Cited</b> | Application/Control No.<br>09/844,673 | Applicant(s)/Patent Under<br>Reexamination<br>THEKKATH, RADHIKA |             |
|                                   | Examiner<br>Kenneth A Gross           | Art Unit<br>2122  | Page 1 of 1 |

**U.S. PATENT DOCUMENTS**

| * |   | Document Number<br>Country Code-Number-Kind Code | Date<br>MM-YYYY | Name            | Classification |
|---|---|--|-----------------|-----------------|----------------|
|   | A | US-5,970,246                                     | 10-1999         | Moughani et al. | 717/128        |
|   | B | US-5,812,868                                     | 09-1998         | Moyer et al.    | 712/23         |
|   | C | US-5,274,811                                     | 12-1993         | Borg et al.     | 717/128        |
|   | D | US-5,621,886                                     | 04-1997         | Alpert et al.   | 714/38         |
|   | E | US-5,978,937                                     | 11-1999         | Miyamori et al. | 714/45         |
|   | F | US-6,314,530                                     | 11-2001         | Mann, Daniel    | 714/38         |
|   | G | US-5,832,515                                     | 11-1998         | Ledain et al.   | 707/202        |
|   | H | US-  |                 |                 |                |
|   | I | US-  |                 |                 |                |
|   | J | US-  |                 |                 |                |
|   | K | US-  |                 |                 |                |
|   | L | US-  |                 |                 |                |
|   | M | US-  |                 |                 |                |

**FOREIGN PATENT DOCUMENTS**

| * |   | Document Number<br>Country Code-Number-Kind Code | Date<br>MM-YYYY | Country | Name | Classification |
|---|---|--|-----------------|---------|------|----------------|
|   | N |  |                 |         |      |                |
|   | O |  |                 |         |      |                |
|   | P |  |                 |         |      |                |
|   | Q |  |                 |         |      |                |
|   | R |  |                 |         |      |                |
|   | S |  |                 |         |      |                |
|   | T |  |                 |         |      |                |

**NON-PATENT DOCUMENTS**

| * |   | Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)  |
|---|---|--|
|   | U | "MIPS32 Architecture For Programmers Volume I: Introduction to the MIPS32 Architecture", Revision 0.95, March 12, 201, MIPS Technologies, Inc.       |
|   | V | "MIPS32 Architecture For Programmers Volume III: The MIPS32 Privileged Resource Architecture", Revision 0.95, March 12, 201, MIPS Technologies, Inc. |
|   | W | "Trace Control Bus and System Analyzer for MIPS32 4KE and Newer Cores", by Rick Leatherman, 2002, First Silicon Solutions.                           |
|   | X |  |

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.